

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No. : 10/536,732
Applicants : Eric Desmicht
Filed : May 27, 2005
Art Unit : 2432
Examiner : OKEKE, IZUNNA

Confirmation No. 4315

Docket No. : 348162-982430
Cust. No. : 94518

Title: Chip Integrated Protection Means

M/S AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of Transmission Under 37 CFR 1.8
I hereby certify that this correspondence is being transmitted via
electronic submission, attention Mail Stop: AF, Commissioner
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on: March 9, 2011:

/Alan A. Limbach/
Alan A. Limbach

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RESPONSE TO SEPTEMBER 9, 2010 OFFICE ACTION
SUBMITTED WITH RCE

Sir:

In response to the Final Office Action mailed September 9, 2010, and in conjunction with the RCE submitted herewith, please enter the amendments set forth below and consider the following remarks.

Amendments to the Claims are shown in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.